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⑤④ **NON-VOLATILE FAULT DISPLAY WITH MAGNETIC RESET SWITCH FOR ADAPTIVE BRAKING SYSTEM.**

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US-A- 3 975 708
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Description

This invention relates to a fault display and reset mechanism for a vehicle electronic control system, such as an adaptive braking system.

Vehicle electronic control systems, such as adaptive braking systems, normally work quite well and are very reliable. However, because of the environmental and physical stresses to which components of these systems are subjected due to their being mounted in the adverse environment on the exterior of the vehicle, such systems occasionally malfunction. Many of these malfunctions are rather simple to correct. For example, a common malfunction is a shorted or open sensor or modulator, which may be caused by a broken wire, a connector being vibrated loose, etc. Such malfunctions are relatively simple for even a relatively untrained mechanic to correct, but until now the only warning of such a malfunction was a warning light in the vehicle operator's compartment. Furthermore, some malfunctions are intermittent. Since the warning device is activated only during such a malfunction, upon vehicle shutdown and subsequent powerup the warning device is not activated until the malfunction occurs again. Accordingly, there is no record of such a malfunction, so that even if the vehicle was serviced in the interim the defect probably would not have been either noted or corrected.

Systems for diagnosing faults in vehicle electronic systems are known in the prior art. For example, GB-A-2 125 577 discloses a fault indicating mechanism for an electronic system, said electronic system including a housing and electronic components within said housing, said housing protecting said components from environmental contaminants, an electronic display mounted within said housing but visible from the exterior thereof for indicating malfunctions of one or more components of said electronic system, and a reset mechanism for resetting said display after the malfunctions indicated thereby are corrected.

According to the present invention, a series of light emitting diodes (LEDs) are provided on the housing within which the electronic control unit component of the system is mounted. Each of the light emitting diodes is activated in response to a predetermined discrete fault in the system. For example, one of the light emitting diodes may be activated in response to a fault in one of the wheel speed sensors, and another of the light emitting diodes may be activated in response to a malfunction in one of the modulators. Accordingly, a relatively unskilled mechanic is able to examine the wires, connectors, etc. associated with the corresponding wheel speed sensor or modulator. If a broken wire is found or a loose connector is discovered, the mechanic can quickly repair the system, perhaps in the field, thereby saving valuable time that would ordinarily be required to send the vehicle to a maintenance location to perform more sophisticated

diagnostic tasks. Of course, if the mechanic is unsuccessful in locating such a simple failure, or if the light emitting diode corresponding to a fault in, for example, the system logic controller is activated, the driver knows immediately that the vehicle must be taken to a maintenance location where a mechanic skilled in repairing electronic systems is available.

Accordingly, the present invention is characterized in that said reset mechanism is sealed within said housing and inaccessible from outside of said housing without disassembly of the housing for resetting said electronic display, said reset mechanism including a magnetically responsive component covered by the housing which responds to a magnet held adjacent that portion of the housing covering the magnetically responsive component.

According to another feature of the invention, the faults sensed by the system and indicated on the light emitting diodes are stored in a non-volatile memory when the system is powered down when the vehicle is parked. Accordingly, once a malfunction occurs, the malfunction is stored in memory, and the corresponding indicator is activated upon system powerup at a later time.

Accordingly, if a transient or intermittent failure does occur, the mechanic repairing the system will immediately know that such a fault has occurred at some time during the operation of the vehicle.

Of course, since the faults are stored in the non-volatile memory, it is, of course, necessary that the mechanic be able to clear the faults when the system has been repaired. Normally this would be done by providing a reset switch on the housing or elsewhere on the vehicle. However, such reset switches in themselves are a cause of malfunctions. Since the system is mounted on the exterior of the vehicle, the mechanical-type reset switch must penetrate the housing, thereby resulting in a less than satisfactory environmental seal. According to the present invention, a magnetic reset switch is provided within the housing, which is sealed against environmental contaminants, but is located adjacent to the wall of the housing. A trained mechanic knows the approximate location of the magnetic reset switch, and can clear the faults merely by passing a common magnet over the area of the housing within which the magnetic reset switch is located. By housing the switch within the housing, the contacts of the device are protected from, or immune to, oxidation and corrosion of the electrical contacts resulting from exposure of the device to the atmosphere. Furthermore, the magnetic reset switch provides tamper resistance due to the fact that no exterior switch button is visible to the user. A trained mechanic can easily activate the switch because he has been trained as to its location and the manner in which a magnet must be placed near the portion of the housing within which the switch is mounted in order to reset the switch. However, the vehicle driver, or another

untrained person, cannot easily tell how to reset the indicators.

These and other features of the invention will become apparent from the following description, with reference to the accompanying drawings, in which:

Figure 1 is a side view of a housing for a vehicle electronic control system, illustrating the location of the indicators and reset switch according to the present invention;

Figure 2 is a diagrammatic illustration of the various components of an adaptive braking system including a failure indicating mechanism according to the present invention;

Figure 3 is a diagrammatic view of the various functions performed by the microprocessor used in the present invention; and

Figures 4-7 are diagrammatic flow charts illustrating the sequence of operation of the system for indicating faults according to the present invention.

Referring now to Figure 1 of the drawings, the electronic control unit which comprises a part of a larger electronic system is housed within a housing generally indicated by the numeral 10. Housing 10 is provided with sealed apertures exposing a series of light emitting diodes 12. A magnetic reset switch which, as will be described hereinafter, is used to reset the faults indicated by the light emitting diodes 12, is illustrated in phantom as at 14. The magnetic reset switch 14 is sealed within the housing 10, but is mounted adjacent to the wall thereof, where it can respond to a magnetic passed along the exterior of the housing 10. The housing 10 further includes ports (not shown) for connection to vehicle speed sensors and adaptive braking pressure modulators (not shown in Figure 1).

Referring to Figure 2, the adaptive braking system is illustrated diagrammatically as at 16. The system 16 includes a microprocessor 18 which is mounted within the housing 10. The microprocessor 18 is connected to one or more speed sensors 20 through the ports (not shown) on the housing 10. The speed sensors 20 may either be a conventional drive line speed sensor mounted in the vehicle transmission or differential, or may be wheel speed sensors mounted at the vehicle wheels. Such sensors are conventional, and provide a pulsed output which is transmitted to the microprocessor 18, the frequency of the pulses being proportional to wheel or drive line speed. The microprocessor 18 processes the signals received from the speed sensors 20 and generates output signals which control one or more brake pressure modulators 22. Brake pressure modulators 22 are also conventional, and are adapted to decrease and thereafter increase braking pressure in response to incipient skidding conditions detected by the microprocessor 18 in response to the signals generated by the speed sensors 20. Microprocessor 18 also

includes diagnostics which check the microprocessor 18, the modulators 22, and the speed sensors 20 for faults. In response to such faults, the microprocessor 18 activates one or more of the light emitting diodes 12, which are provided to indicate faults in the speed sensors, the modulators, or the microprocessor. A feature of the invention is that one or more discrete light emitting diodes is provided for each of the possible faults detected, so that the mechanic servicing the vehicle can immediately tell, by examining the status of the light emitting diodes 12, which of the components of the system is malfunctioning. Since many of the malfunctions sensed by the aforementioned diagnostics are of a transient or temporary nature, and which are eliminated when the system powers down upon vehicle shutoff, a non-volatile random access memory 24 is provided to store the faults detected by the aforementioned diagnostics, so that upon powerup when the vehicle is started the same light emitting diodes 12 are again activated as were activated before shut down. The magnetic reset switch 14 is connected through the microprocessor and resets the light emitting diodes 12 when the vehicle has been serviced and the faults no longer exist. The mechanic resets the switch 14, as will hereinafter be described, by passing a magnet 16 across that portion of the housing 10 within which the magnetic reset switch 14 is mounted. The non-volatile memory 24 is connected to the microprocessor 18 through the necessary data transmission lines and control lines generally indicated at 26.

Referring now to Figure 3, the microprocessor generally indicated by the numeral 18 includes an input signal conditioning routine generally indicated by the numeral 28 which conditions and checks the signals received from the speed sensors 20. The input signal conditioning routine 28 transmits signals representing speed to the antilock computation section of the microprocessor generally indicated by the numeral 30. The antilock computation section 30 generates output signals for transmission to the modulators 22 through an output conditioning routine 32 according to well established principles of wheel lock control used in adaptive braking systems. A diagnostic and shutdown routine generally indicated by 34 monitors the input signal conditioning 28, the antiskid computation section 30, and the output signal conditioning routine 32 to check for malfunctions in any of the circuitry or routines within the microprocessor 18 and is also capable, by examining the signals transmitted to the input conditioning routine 28 or transmitted to the modulator 22, to establish faults in the sensors 20 or the modulators 22. Upon sensing such a fault, the diagnostic routine 34 shuts down the system, activates a warning device (not shown) on the vehicle operator's compartment, and also activates one or more of the light emitting diodes 12, through a LED display and reset routine generally indicated by the

numeral 36. This routine controls the activity and resetting of the light emitting diodes 12, and also controls communication of the various faults to the non-volatile memory 24.

Referring now to Figure 4, the LED display and reset routine 36 cycles through the program illustrated in Figure 4 every 10 to 30 milliseconds (depending upon the speed of the vehicle) in order to check for faults and to display such faults through the light emitting diodes 12. The microprocessor 18 includes an internal memory, in the routine illustrated diagrammatically in Figure 4, this internal memory includes two sections called "old failures" and "new failures". The "old failures" section stores failures detected on the previous cycle through the routine, and the "new failures" section stores failures detected on the present cycle.

The update failure routine illustrated in Figure 4 is executed every 10 to 30 milliseconds (depending upon the output of the speed sensor) during operation of the vehicle. Referring to Figure 4, the new failures (that is, the failure flags set on the most recent pass through the routine) are cleared from memory as indicated at 38. The diagnostic section 34 of the microprocessor is checked for modulator failures as indicated at 40, speed sensor failures as indicated at 42, and electronic failures as indicated at 44. If any such failures are detected, they are stored in memory as new failures as indicated at 46. As indicated at 48, the old failures section of the memory is set equal to all of the old failures already stored in memory plus the new failures as from 46. The NV CLEAR flag (see Figure 6) is then tested as indicated at 50. If the NV CLEAR flag has been set, all of the LEDs are loaded on as indicated at 52. As will be described hereinafter with reference to Figure 6, the NV CLEAR flag is set while the aforementioned magnet is held against the magnetic reset switch 14. All of the LEDs, regardless of the failures, are then turned on to indicate to the mechanic that the failures have been cleared.

If the NV CLEAR flag has not been set, the old failures are loaded for the output to the LEDs as indicated at 54. Accordingly, the LEDs are turned on which correspond to the various failures set in old failures. Memory is then tested, as indicated at 56 to determine if there are any failures stored in memory, if there are failures, the shut down flag is set as at 58. This flag is used by the microprocessor to turn off the adaptive braking because of the failure. If there are no failures, the routine branches around 58. The program again tests to determine if there are failures as indicated at 60. If there are failures, the warning lamp in the vehicle operator's compartment is turned on as indicated at 62, to warn the driver that the adaptive braking system has been disabled. If there are no failure flags set, the warning lamp is turned off as indicated at 64. The warning lamp is then tested as at 66 to determine if it has been turned off or not. If the warn-

ing lamp is still on, after it should have been turned off at 64, the routine branches to block 62 so that the warning light is turned back on, in view of the warning lamp failure. If the warning lamp is off, the routine calls the update NVRAM subroutine illustrated in Figure 5, as indicated at 68, before the routine ends.

Referring now to Figure 5, the update NVRAM subroutine first tests the NV UPDATE flag as indicated at 70. If the NV UPDATE flag has not been set, the routine then tests to determine if there are any new failure flags set, as indicated at 72. If the NV UPDATE flag has been set (see Figure 6), the routine branches around decision block 72. The NV UPDATE flag indicates that all the faults were cleared by actuating the switch 14 on the last pass through the routine illustrated in Figure 4. The routine illustrated in Figure 5 then enables the system to write the data to the non-volatile RAM 24, as indicated at 74. The failure flags are then called from memory as indicated at 76, and the data is written to the non-volatile RAM as indicated at 78. A command is then transmitted over one of the control lines to the non-volatile RAM 24 to make the data written to the non-volatile RAM non-volatile, as indicated at 80. Accordingly, the data will be retained in memory even if the system is powered down. The NV UPDATE flag is then cleared as indicated at 82. Referring back to decision block 72, if there are no new failures, the routine branches to the end.

When a mechanic resets the failure indicating light emitting diodes (LEDs) 12, the routine illustrated in Figure 6 resets the light emitting diodes (LEDs) 12. A check is made to see if the NV CLEAR input is activated, as indicated at 84. The NV CLEAR input is the input generated by the magnetic reset switch 14 when the reset switch 14 is activated by a magnet held by the mechanic against the housing adjacent to the reset switch 14, as discussed hereinabove. If the NV CLEAR input has been activated, the NV reset count is then incremented by 10 milliseconds as indicated at 86. The NV reset count is a counter which is incremented every time a pass is made through the routine as illustrated in Figure 6 while the magnet is being held against the housing. The reset count is then tested, as indicated at 88, to see if it is equal to 30 milliseconds. In other words, incrementing the counter 86 in the decision block 88 requires that the magnet be held against the housing adjacent to the reset switch for at least 30 milliseconds. This prevents the failure indicators from being inadvertently cleared by stray magnetic signals. If the magnet was not held against the housing to activate the switch 14 for at least 30 milliseconds, the program branches to the end. However, if the magnet has been held adjacent the switch 14 for at least 30 milliseconds, the NV CLEAR flag is set as indicated at 90. As discussed hereinabove with respect to Figure 4, the NV CLEAR flag causes all of the indicators 12 to be turned on when the faults are cleared, thus indicating to the

mechanic that the faults have indeed been cleared. All failure flags are then cleared as indicated at 92, and the shut down flag discussed with respect to Figure 4 is also cleared as indicated at 94 before the routine ends.

The routine illustrated in Figure 6 is cycled approximately every 10 milliseconds while the electronic system is being operated. Normally, the magnetic reset switch will not be activated and the routine branches from decision block 84 to decision block 96. During normal operation of the system, the reset count will have been reset equal to 0, which is effected as indicated at 88. However, the first time through the routine after the aforementioned magnet has been removed from adjacent the magnetic reset switch 14, the reset count will not have been reset. Accordingly, the NV CLEAR flag is cleared as indicated at 98, and the NV UPDATE flag is set as indicated at 100. As discussed hereinabove, the NV UPDATE flag is tested in the routine illustrated in Figure 5. The NV reset count is then cleared as indicated at 102. Accordingly, the routine illustrated in Figure 6 clears all failures and turns on all of the indicators 12 when the magnet has been held adjacent to the switch 14 for a minimum of 30 milliseconds, and then turns off all of the indicators when the magnet is removed from adjacent the switch 14 after being held against the housing adjacent to switch 14 for a minimum of 30 milliseconds.

The rather short routine illustrated in Figure 7 is performed only once upon vehicle system powerup. The data stored in the non-volatile RAM is read as indicated at 104 and is put into the new failure flags section of the memory in the microprocessor as indicated in 106, before the subroutine ends.

Claims

1. Fault indicating mechanism for an electronic system, said electronic system including a housing (10) and electronic components (18) within said housing (10), said housing (10) protecting said components (18) from environmental contaminants, an electronic display (12) mounted within said housing (10) but visible from the exterior thereof for indicating malfunctions of one or more components (18) of said electronic system, and a reset mechanism (14,18) for resetting said display (12) after the malfunctions indicated thereby are corrected, characterized in that said reset mechanism (14,18) is sealed within said housing (10) and inaccessible from outside of said housing (10) without disassembly of the housing (10) for resetting said electronic display (12), said reset mechanism (14,18) including a magnetically responsive component (14) covered by the housing (10) which responds to a magnet (16) held adjacent that portion of the housing (10) covering the magnetically responsive component (14).

2. Fault indicating mechanism as claimed in claim 1, further characterized in that said system includes memory means (24) for storing records of said malfunctions even if power to the system is turned off and for causing said electronic display (12) to again display the malfunctions displayed before the power was turned off when the power is again turned on.

3. Fault indicating mechanism as claimed in claim 2, further characterized in that said system includes diagnostic means (34) for sensing malfunctions of said components (18), said memory means (24) including means for memorizing each malfunction sensed by said diagnostic means (34) and causing said electronic display (12) to indicate said malfunction even if the malfunction disappears.

4. Fault indicating mechanism as claimed in claim 3, further characterized in that said electronic display (12) includes a series of discrete indicators (12), each of said indicators (12) corresponding to a predetermined type of malfunction.

5. Fault indicating mechanism as claimed in claim 4, further characterized in that said discrete indicators (12) are light emitting diodes.

6. Fault indicating mechanism as claimed in any of claims 1-5, further characterized in that said system is a vehicle adaptive braking system including vehicle speed sensing means (20), brake pressure modulator means (22), and control means responsive to said speed sensing means (20) for operating said modulator means (22), said discrete indicators (12) including at least one indicator for indicating a malfunction in one of said speed sensing means (20) and at least one indicator for indicating a malfunction of said modulator means (22).

7. Fault indicating mechanism as claimed in claims 4 or 5, further characterized in that said reset mechanism (14,18) includes means for actuating all of said discrete indicators (12) when said magnetically activated means responds to said magnet (16) held adjacent the housing (10), and then extinguishes said indicators (12) when the magnet (16) is then moved away from said housing (10).

8. Fault indicating mechanism as claimed in claim 1, further characterized in that said electronic display (12) includes a series of discrete indicators (12), each of said indicators (12) corresponding to a predetermined type of malfunction.

9. Fault indicating mechanism as claimed in claim 8, further characterized in that said reset mechanism (14,18) includes means for actuating all of said discrete indicators (12) when said magnetically activated means responds to said magnet (16) held adjacent the housing (10), and then extinguishes said indicators (12) when the magnet (16) is then moved away from said housing (10).

10. Fault indicating mechanism as claimed in claim 9, further characterized in that said system includes diagnostic means (34) for sensing malfunc-

tions of said components (18) and memory means (24) for storing records of said malfunctions, said memory means (24) including means for memorizing each malfunction sensed by said diagnostic means (34) and causing said electronic display (12) to indicate said malfunction even if the malfunction disappears.

Patentansprüche

1. Fehleranzeigemechanismus für eine elektronische Anordnung, welche ein Gehäuse (10) und elektronische Bauteile (18) in dem Gehäuse (10) umfaßt, wobei das Gehäuse (10) die Bauteile (18) gegen Umgebungsverunreinigungen schützt, eine elektronische Anzeige (12), die in dem Gehäuse (10) angebracht, jedoch von außen sichtbar ist, um Falscharbeiten eines oder mehrerer der Bauteile (18) der elektronischen Anordnung anzuzeigen, und einen Rücksetzmechanismus (14,18) umfaßt zum Rücksetzen der Anzeige (12), nachdem das von ihr angezeigte Falscharbeiten korrigiert ist, dadurch gekennzeichnet, daß der Rücksetzmechanismus (14,18) in dem Gehäuse (10) abgedichtet und von der Außenseite des Gehäuses (10) her zum Rücksetzen der elektronischen Anzeige (12) nicht zugänglich ist, ohne das Gehäuse (10) auseinanderzunehmen, wobei der Rücksetzmechanismus (14,18) einen magnetisch ansprechenden Bauteil (14), der von dem Gehäuse (10) abgedeckt ist, umfaßt, der auf einen Magneten (16) anspricht, der nahe desjenigen Teiles des Gehäuses (10) gehalten ist, der den magnetisch ansprechenden Bauteil (14) abdeckt.

2. Fehleranzeigemechanismus nach Anspruch 1, weiter dadurch gekennzeichnet, daß die Anordnung eine Speichereinrichtung (24) umfaßt, um Aufzeichnungen des Falscharbeitens zu speichern, selbst wenn die Energiezufuhr zu der Anordnung abgeschaltet ist, und um die elektronische Anzeige (12) zu veranlassen, das Falscharbeiten, welches vor dem Abschalten der Energie angezeigt worden ist, erneut anzuzeigen, wenn die Energie wiederum angeschaltet wird.

3. Fehleranzeigemechanismus nach Anspruch 2, weiter dadurch gekennzeichnet, daß die Anordnung eine Diagnoseeinrichtung (34) zum Feststellen von Falscharbeiten der Bauteile (18) umfaßt, und die Speichereinrichtung (24) Mittel zum Speichern Jedes Falscharbeitens, welches von der Diagnoseeinrichtung (34) festgestellt ist, umfaßt und die elektronische Anzeige (12) veranlaßt, das Falscharbeiten anzuzeigen, selbst wenn das Falscharbeiten aufgehört hat.

4. Fehleranzeigemechanismus nach Anspruch 3, weiter dadurch gekennzeichnet, daß die elektronische Anzeige (12) eine Reihe von diskreten Anzeigern (12) umfaßt, deren jeder einer vorbestimmten Art eines Falscharbeitens entspricht.

5. Fehleranzeigemechanismus nach Anspruch 4, weiter dadurch gekennzeichnet, daß die diskreten Anzeiger (12) lichtemittierende Dioden sind.

6. Fehleranzeigemechanismus nach irgendeinem der Ansprüche 1 bis 5, weiter dadurch gekennzeichnet, daß die Anordnung eine an ein Fahrzeug anpaßbare Bremsanlage ist umfassend Einrichtungen (20) zum Feststellen der Fahrzeuggeschwindigkeit, eine Bremsdruckmoduliereinrichtung (22), und eine Steuereinrichtung, die auf die Geschwindigkeitsfeststelleinrichtungen (20) anspricht, um die Moduliereinrichtung (22) zu betätigen, wobei die diskreten Anzeiger (12) wenigstens einen Anzeiger zum Anzeigen eines Falscharbeitens einer der Geschwindigkeitsfeststelleinrichtungen (20) und wenigstens einen Anzeiger umfaßt zum Anzeigen eines Falscharbeitens der Moduliereinrichtung (22).

7. Fehleranzeigemechanismus nach Anspruch 4 oder 5, weiter dadurch gekennzeichnet, daß der Rücksetzmechanismus (14,18) Mittel zum Betätigen aller diskreter Anzeiger (12), wenn die magnetisch aktivierte Einrichtung auf den nahe dem Gehäuse (10) gehaltenen Magneten (16) anspricht, umfaßt und dann die Anzeiger (12) löscht bzw. unwirksam macht, wenn der Magnet (16) dann von dem Gehäuse (10) weg bewegt wird.

8. Fehleranzeigemechanismus nach Anspruch 1, weiter dadurch gekennzeichnet, daß die elektronische Anzeige (12) eine Reihe von diskreten Anzeigern (12) umfaßt, deren jeder einer vorbestimmten Art eines Falscharbeitens entspricht.

9. Fehleranzeigemechanismus nach Anspruch 8, weiter dadurch gekennzeichnet, daß der Rücksetzmechanismus (14,18) Mittel zum Betätigen aller diskreter Anzeiger (12), wenn die magnetisch aktivierte Einrichtung auf den nahe dem Gehäuse (10) gehaltenen Magneten (16) anspricht, umfaßt und dann die Anzeiger (12) löscht bzw. unwirksam macht, wenn der Magnet (16) dann von dem Gehäuse (10) weg bewegt wird.

10. Fehleranzeigemechanismus nach Anspruch 9, weiter dadurch gekennzeichnet, daß die Anlage eine Diagnoseeinrichtung (34) zum Feststellen von Falscharbeiten der Bauteile (18) und eine Speichereinrichtung (24) zum Speichern von Aufzeichnungen des Falscharbeitens umfaßt, und die Speichereinrichtung (24) Mittel zum Speichern Jedes von der Diagnoseeinrichtung (34) festgestellten Falscharbeitens umfaßt und die elektronische Anzeige (12) veranlaßt, das Falscharbeiten anzuzeigen, selbst wenn das Falscharbeiten verschwunden ist bzw. aufgehört hat.

Révendications

1. Mécanisme d'indication de défauts pour un système électronique, ledit système électronique

comprenant un boîtier (10) et des composants électroniques (18) situés dans ledit boîtier (10), ledit boîtier (10) protégeant lesdits composants (18) vis-à-vis de substances polluantes de l'environnement, un dispositif d'affichage électronique (12) monté dans ledit boîtier (10), mais visible de l'extérieur de ce dernier et indiquant des défauts de fonctionnement d'un ou de plusieurs composants (18) dudit système électronique, et un mécanisme de remise à zéro (14,18) servant à ramener à zéro ledit dispositif d'affichage (12) après correction des défauts de fonctionnement qu'il a indiqués, caractérisé en ce que ledit mécanisme de remise à zéro (14,18) est enfermé de façon étanche dans ledit boîtier (10) et n'est pas accessible de l'extérieur dudit boîtier (10) sans démontage de ce dernier, pour la remise à zéro du dispositif d'affichage électronique (12), ledit mécanisme de remise à zéro (14,18) comprenant un composant (14) magnéto-sensible et entouré par le boîtier (10) et qui répond à un aimant (16) maintenu en position adjacente à la partie du boîtier (10), qui recouvre le composant magnéto-sensible (14).

2. Mécanisme d'indication de défauts selon la revendication 1, caractérisé en outre en ce que ledit système comprend des moyens de mémoire (24) servant à mémoriser des enregistrements desdits défauts de fonctionnement même si l'application de l'énergie au système est interrompue, et pour amener ledit dispositif d'affichage électronique (12) à afficher à nouveau les défauts de fonctionnement affichés avant l'interruption de l'énergie, lorsque l'énergie est à nouveau appliquée.

3. Mécanisme d'indication de défauts selon la revendication 2, caractérisé en outre en ce que ledit système comprend des moyens de diagnostic (34) servant à détecter des défauts de fonctionnement desdits composants (18), lesdits moyens de mémoire (24) comprenant des moyens pour mémoriser chaque défaut de fonctionnement détecté par lesdits moyens de diagnostic (34) et à amener ledit dispositif d'affichage électronique (12) à indiquer ledit défaut de fonctionnement même si ce dernier disparaît.

4. Mécanisme d'indication de défauts selon la revendication 3, caractérisé en outre en ce que ledit dispositif d'affichage électronique (12) comprend une série d'indicateurs distincts (12), dont chacun correspond à un type prédéterminé de défaut de fonctionnement.

5. Mécanisme d'indication de défauts selon la revendication 4, caractérisé en outre en ce que lesdits indicateurs de défauts (12) sont des diodes photoémissives.

6. Mécanisme d'indication de défauts selon l'une quelconque des revendications 1-5, caractérisé en ce que ledit système est un système adaptatif de freinage de véhicule comprenant des moyens (20) de détection de la vitesse du véhicule, des moyens (22) de modulation de la pression de freinage et des

moyens de commande répondant auxdits moyens (20) de détection de la vitesse pour actionner lesdits moyens de modulation (22), lesdits indicateurs distincts (12) comprenant au moins un indicateur servant à indiquer un défaut de fonctionnement dans l'un desdits moyens (20) de détection de la vitesse et au moins un indicateur pour indiquer un défaut de fonctionnement desdits moyens de modulation (22).

7. Mécanisme d'indication de défauts selon la revendication 4 ou 5, caractérisé en outre en ce que ledit mécanisme de remise à zéro (14,18) comprend des moyens pour actionner l'ensemble desdits indicateurs distincts (12) lorsque lesdits moyens actionnés magnétiquement répondent audit aimant (16) maintenu en position adjacente au boîtier (10), puis éteignent lesdits indicateurs (12) lorsque l'aimant (16) est écarté dudit boîtier (10).

8. Mécanisme d'indication de défauts selon la revendication 1, caractérisé en outre en ce que ledit dispositif d'affichage électronique (12) comprend une série d'indicateurs distincts (12), dont chacun correspond à un type prédéterminé de défaut de fonctionnement.

9. Mécanisme d'indication de défauts selon la revendication 8, caractérisé en outre en ce que ledit mécanisme de remise à zéro (14,18) comprend des moyens pour actionner tout l'ensemble desdits indicateurs distincts (12) lorsque lesdits moyens actionnés magnétiquement répondent audit aimant (16) maintenu en position adjacente au boîtier (10), puis éteignent lesdits indicateurs (12) lorsque l'aimant (16) est écarté dudit boîtier (10).

10. Mécanisme d'indication de défauts selon la revendication 9, caractérisé en outre en ce que ledit système comprend des moyens de diagnostic (34) pour détecter des défauts de fonctionnement desdits composants (18) et des moyens de mémoire (24) pour mémoriser des enregistrements desdits défauts de fonctionnement, lesdits moyens de mémoire (24) comprenant des moyens pour mémoriser chaque défaut de fonctionnement détecté par lesdits moyens de diagnostic (34) et amener ledit dispositif d'affichage électronique (12) à indiquer ledit défaut de fonctionnement même si ce dernier disparaît.

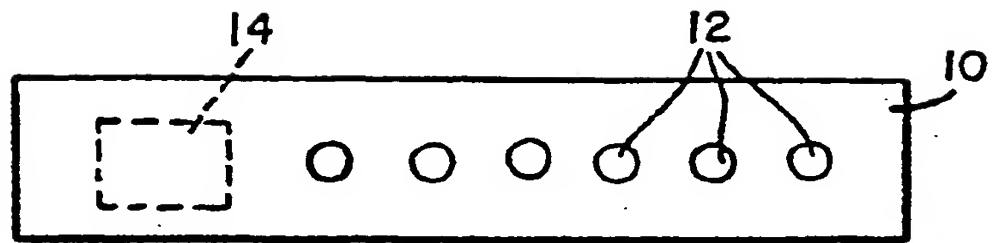


FIG. 1

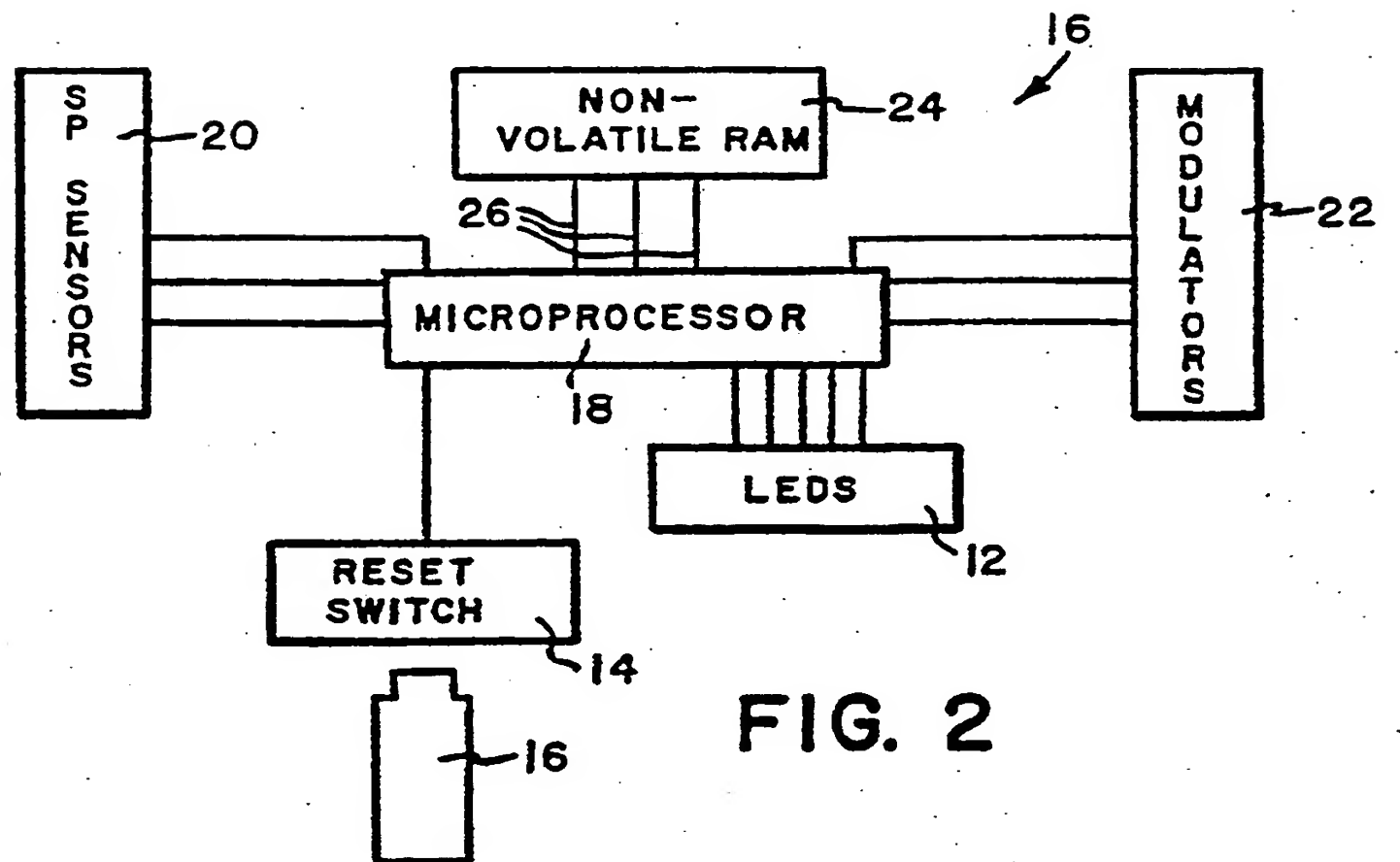


FIG. 2

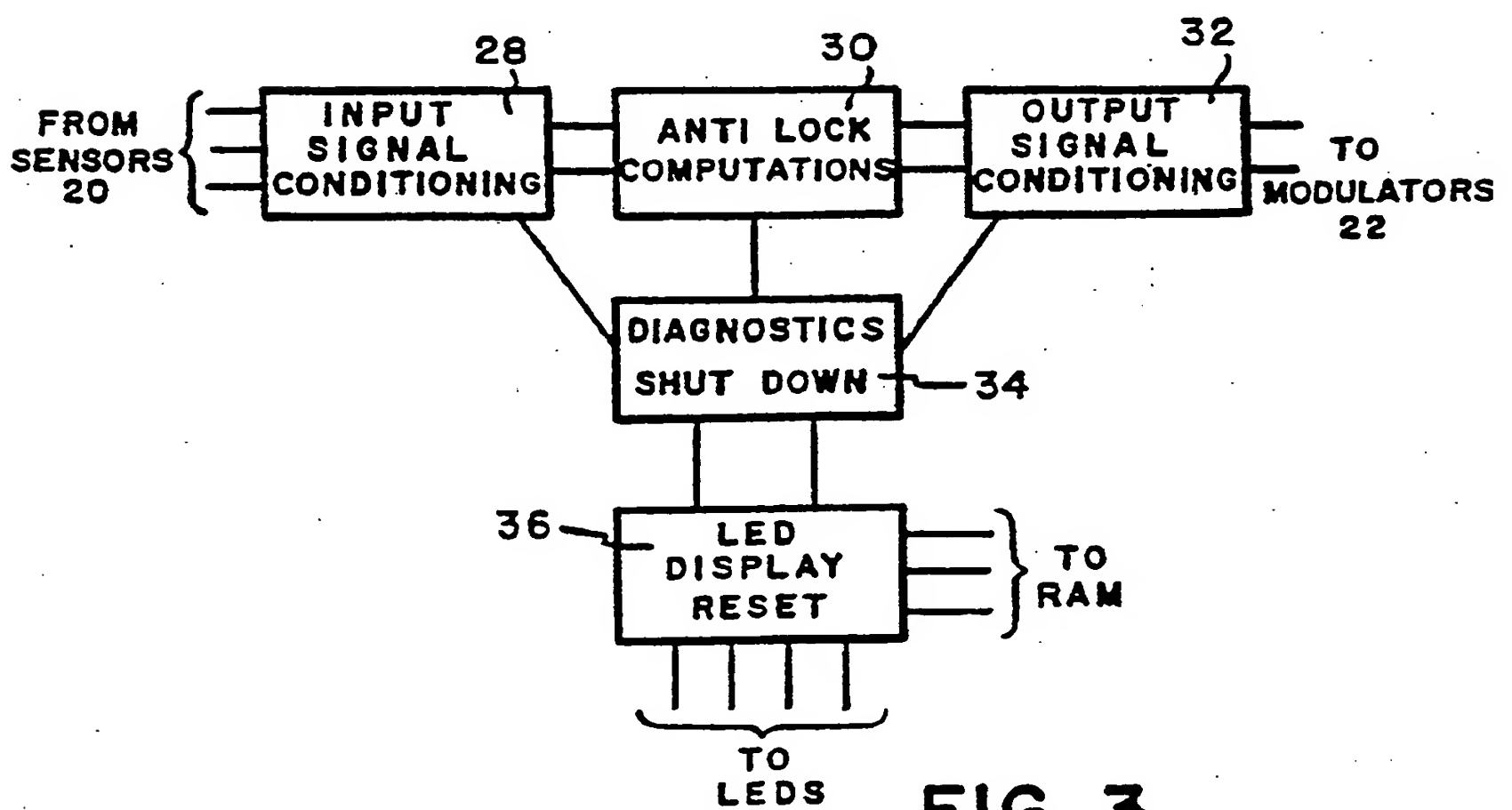


FIG. 3

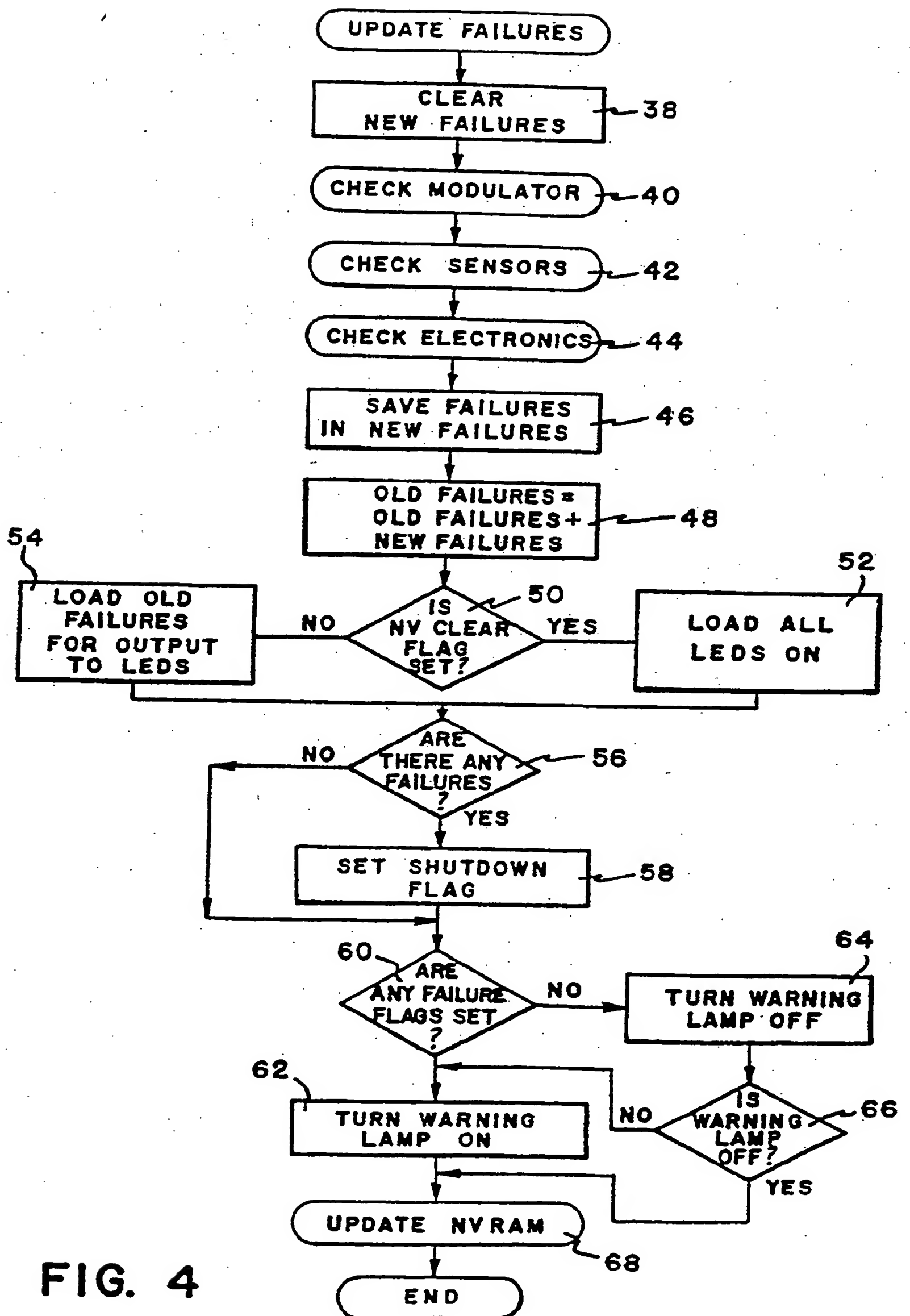


FIG. 4

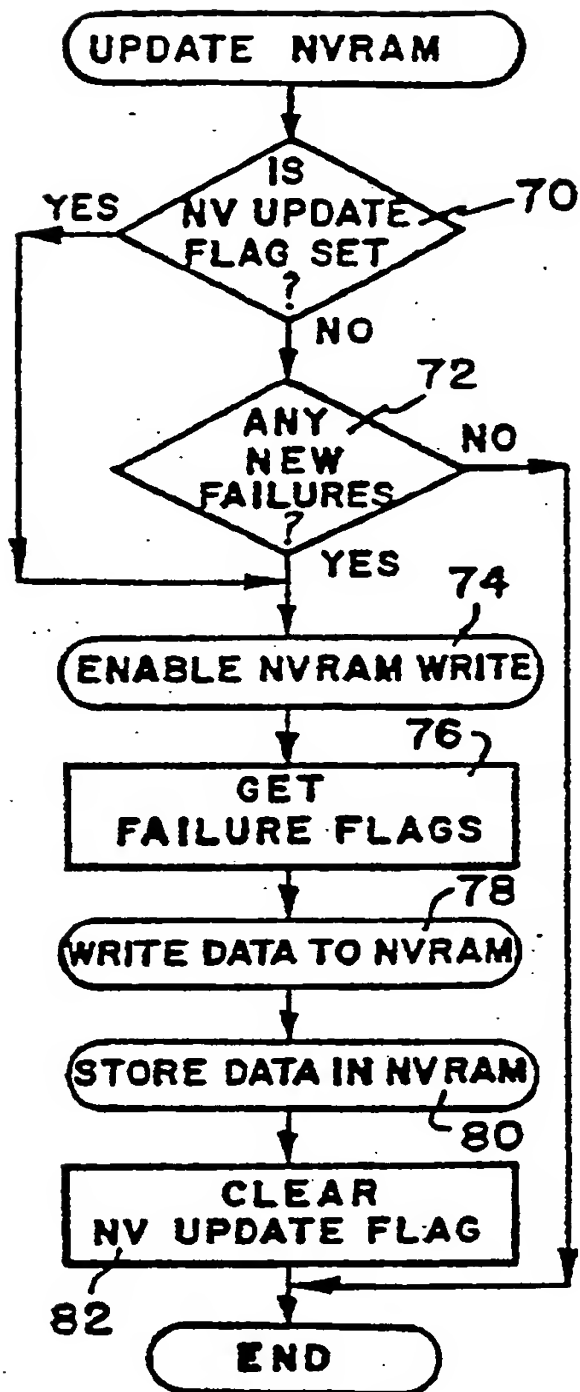


FIG. 5

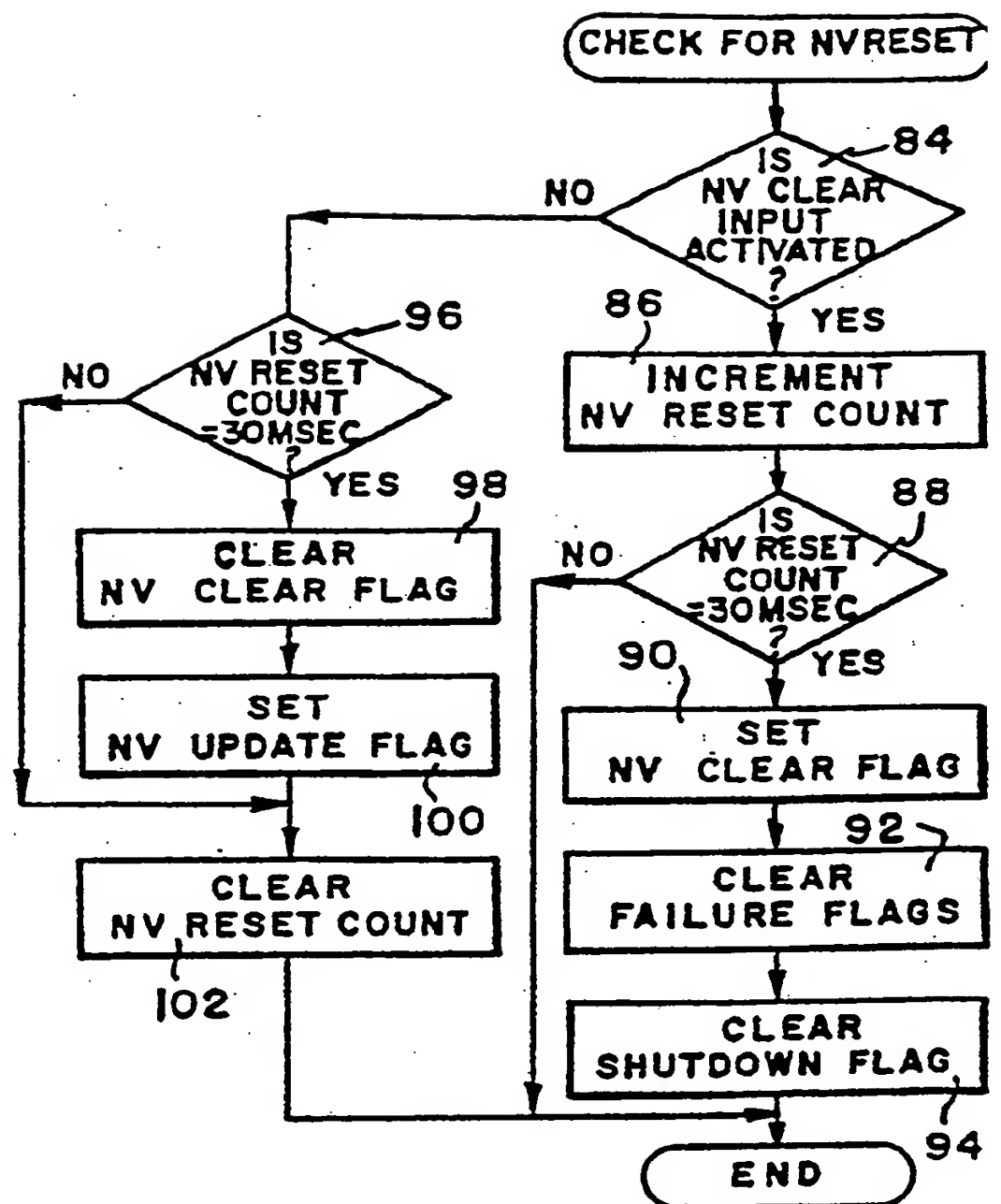


FIG. 6

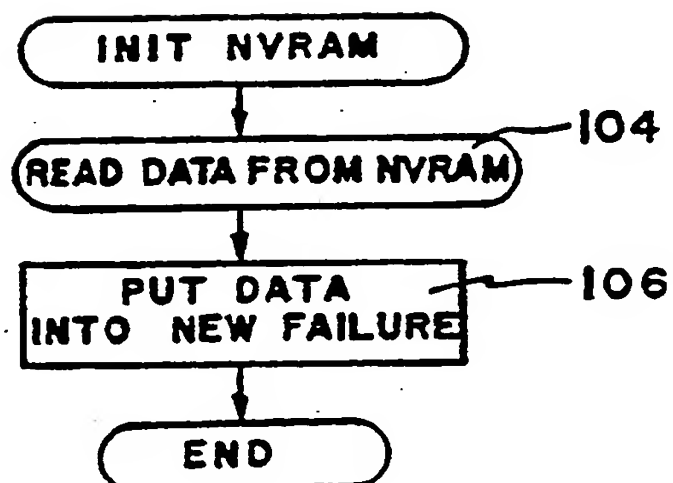


FIG. 7